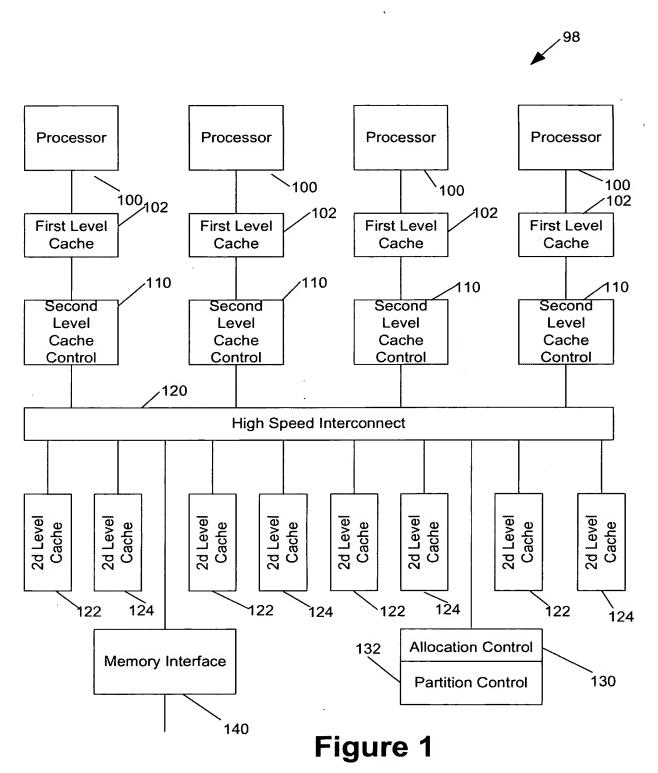
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Large-Scale Multing Lided, Multiprocessor Integrated Circuits
Inventor: Donald C Soltis -- Docket No. 10016693 -- William J. Streeter (970) 898-3886

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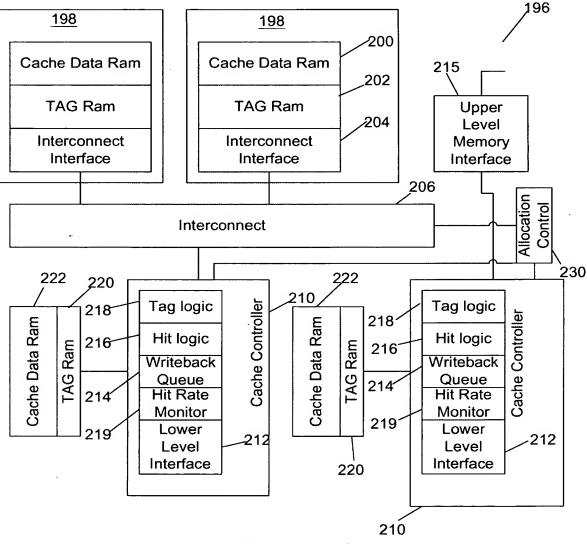


Figure 2

Title: System And M. For Dynamic Processor Core And Cache Partit On Large-Scale Multithrea Jd, Multiprocessor Integrated Circuits
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